

Model 3009 Main Processor Modules

This figure depicts the front panel of the Model 3009 Main Processor.

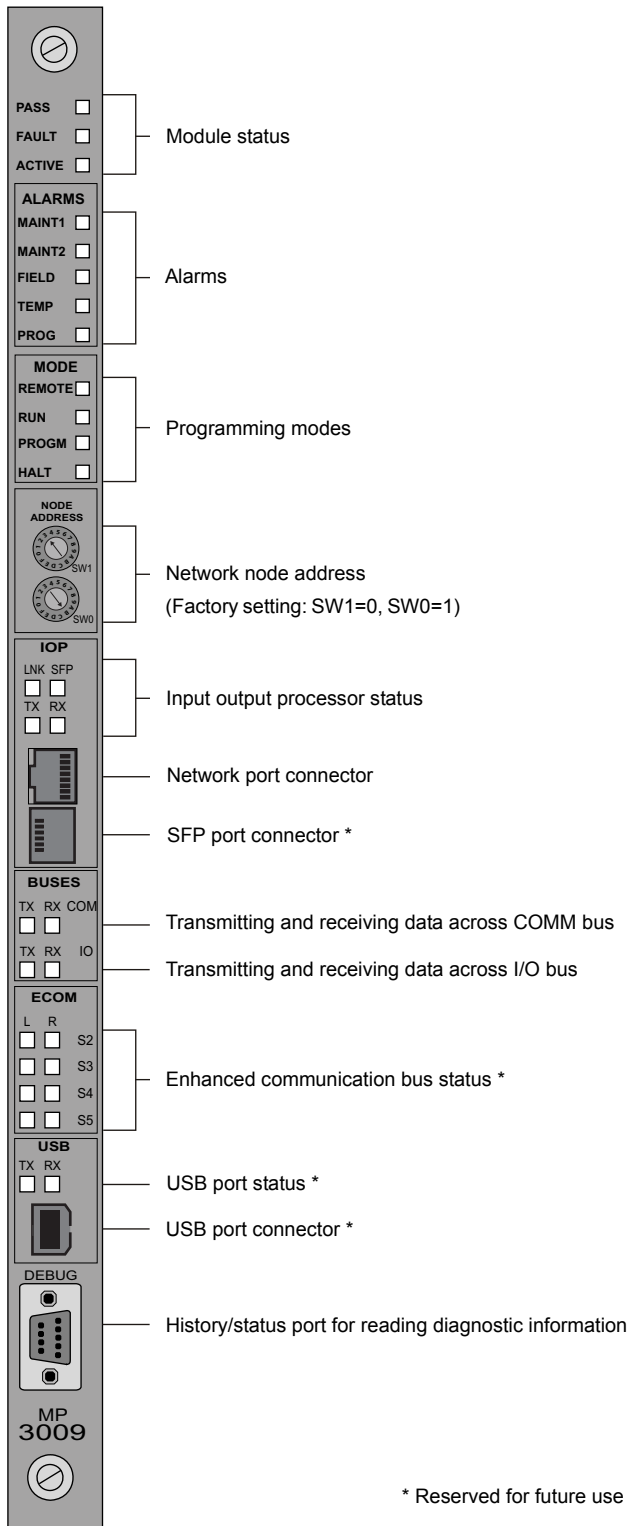


Figure 21 3009 Main Processor Front Panel

3009 Main Processor Architecture

This figure depicts the architecture of the Model 3009 Main Processor, which can be used with Tricon v11.x systems.

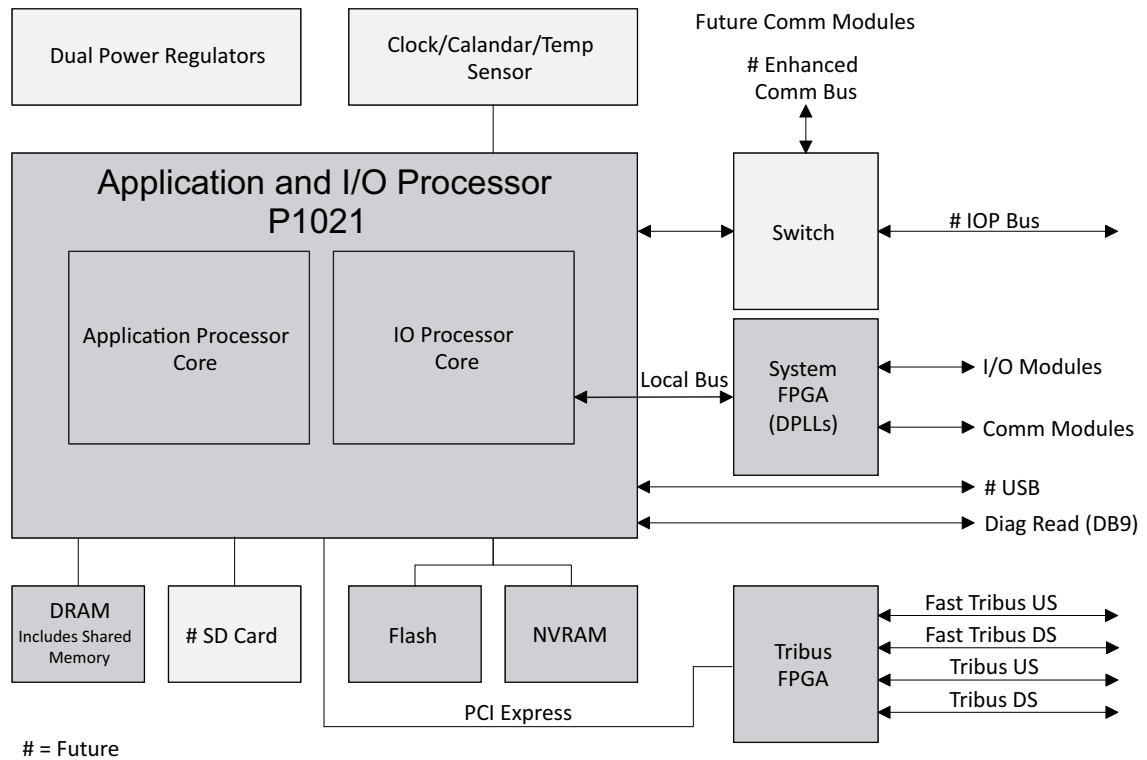


Figure 22 3009 Architecture

3009 Specifications

This table lists the specifications for the Model 3009 Main Processor, which can be used with Tricon v11.x systems.

Table 15 3009 Main Processor Specifications

Feature	Description
Main processor	QorlQ P1021, dual core processor, 32KB L1 cache, 2MB L2 cache, 800 MHz
Memory	256 MB DRAM (without battery backup) 2 MB SRAM (with battery backup) 128 MB Flash PROM
Clock Calendar	Time and date Battery backup Typical drift: ± 2 seconds/day Maximum drift: ± 2.16 seconds/day